

## 7. ANNEX 2 : THE GLOBE CONNECTIONS

This annex explains the configuration of the connections between the FPGA, the board and the external components. As we worked at first on a Zedboard before receiving the Zybo, we explain in this document both configurations. The two following pictures show you the two different ways of naming the ports.



Figure 23: Zybo PMOD configuration

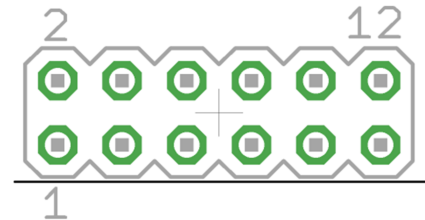


Figure 22: Extension board configuration

The following table shows configurations of the globe on the Zybo and the Zedboard. It also shows where the different functions are connected on the extension board and the globe.

Function	Extension board pin	Zedboard		ZYBO		Globe
		Signal name	Zynq pin	Signal name	Zynq pin	
GND	10, 9					Orange, brown, white
3.3V	12, 11					black, red, pink
Strip 1	2	JA1	Y11	JE1	V12	Violet
Strip 2	4	JA2	AA11	JE2	W16	green
Strip 3	6	JA3	Y10	JE3	J15	yellow
Strip 4	8	JA4	AA9	JE4	H15	Grey
Strip 5	1	JA7	AB11	JE7	V13	Blue
Strip 6	3	JA8	AB10	JE8	U17	Grey blue
Infrared sensor	5	JA9	AB9	JE9	T17	
PWM	7	JA10	AA8	JE10	Y17	

Figure 24: Globe connections