◆ FEATURES

❀ High accuracy, less than ±0.5% error over a dynamic range of 2500: 1

❀ High stability of large signal, in the condition of signal 300mA, less than ± 0.2% error in the output frequency fluctuation (CF).

❀ High stability of small signal, in the condition of signal 50mA, less than ± 0.3% error in the output frequency fluctuation (CF).

❀ Current and voltage RMS, current measurement range (4mA ~ 30A) @ 1mohm

❀ On-Chip anti-creep protection

❀ On-Chip power supply detector

❀ On-Chip voltage reference of 1.2V

❀ On-chip oscillator as clock source, 2MHz

❀ Single 3.3V supply, low power (6 mW typical)

❀ SOP8

◆ DESCRIPTION

BL0937 is a wide range of single-phase multi-function energy meter IC for single-phase plug, Smart Appliance, with a low cost and high accuracy.

BL0937 integrates two high-accuracy Sigma-Delta ADC, voltage reference, power management and other analog circuit modules, and digital signal processing circuit to calculate active power, IRMS, VRMS etc. High frequency CF1 is provided for indicating IRMS/VRMS and high frequency CF for energy metering.

BL0937 can measure the single-phase active energy, active power, current and voltage RMS and other parameters to fully meet the needs of the socket, plug row, smart appliances and others.

Interrelated patents are pending.

◆ BLOCK DIAGRAM

SOP8
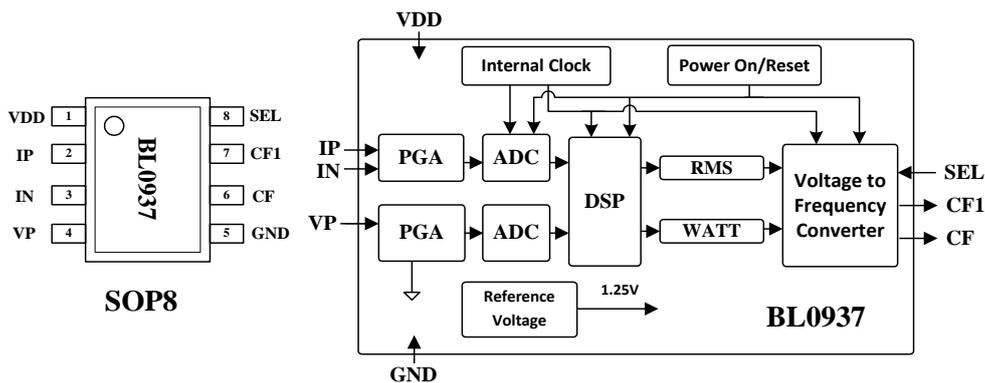
BL0937

◆ PIN DESCRIPTIONS

Pin	Symbol	DESCRIPTIONS
1	VDD	Power Supply (+3.3V). Provides the supply voltage for BL0937 It should be maintained at 3.0V~3.6V when IC is working

2,3	IP,IN	Inputs for Current Channel. These inputs are fully differential voltage inputs with a maximum signal range of ± 50 mV. Because of the internal ESD protection circuit, If the voltage exceeds ± 1.5V the IC will not be damaged badly.
4	VP	Positive Input for Voltage Channel. The Voltage Inputs is differential voltage inputs with a maximum signal range of ± 200 mV. Because of the internal ESD protection circuit, If the voltage exceeds ± 1.5V the IC will not be damaged badly.
5	GND	Ground Reference.
6	CF	High frequency pulse output for active power, the pulse width is fixed to 38uS; the frequency is proportional to the active power value. Over-current indication pin; If over-current occurs, the pin output 6.78KHz pulse.
7	CF1	SEL=0, the output is current RMS, pulse width is fixed to 38uS, The frequency is proportional to the current RMS value; SEL=1, the output is voltage RMS, pulse width is fixed to 38uS, The frequency is proportional to the voltage RMS value;
8	SEL	RMS output select.

◆ **Package Dimensions**

SOP8
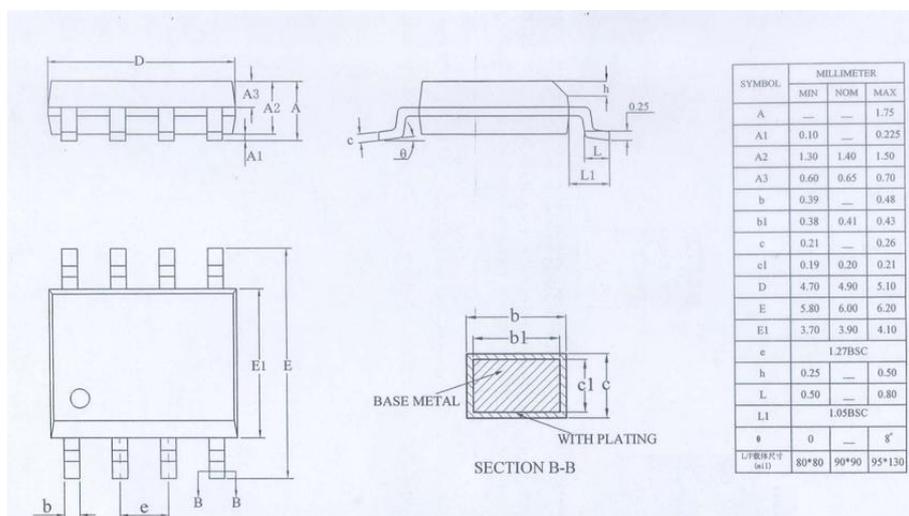
◆ **ABSOLUTE MAXIMUM RATINGS**

（T = 25 ℃）

Parameter	Symbol	Value	Unit
Power Voltage VDD	VDD	-0.3 ~ +4	V
Analog Input Voltage to GND	IP、IN、VP	-4 ~ +4	V
Digital Input Voltage to GND	SEL	-0.3 ~ VDD+0.3	V
Digital Output Voltage to GND	CF、CF1	-0.3 ~ VDD+0.3	V

Operating Temperature Range	Topr	-40 ~ +85	℃
Storage Temperature Range	Tstr	-55~+150	℃
Power Dissipation(SOP8)	P	10	mW

◆ ELECTRONIC CHARACTERISTIC PARAMETER

(VDD=3.3V,GND=0V,On-Chip voltage reference, 2MHz crystal oscillator, Room temperature)

Parameter	Symbol	Test Condition	Measure Pin	Min Value	Typical Value	Max Value	Unit
Power Supply	VDD			3.0		3.6	V
Power Dissipation	Iop	VDD=3.3V			3		mA
Measure Error on Active Power(Absolute error)	WATTerr	a dynamic range 2500 to 1	CF		0.3	0.5	%
Measure fluctuation error on Active Power(Large signal)	Δ@6%Ib, Ib=5A	300mA@1mohm,	CF		0.1	0.2	%
Measure fluctuation error on Active Power(Small signal)	Δ@1%Ib, Ib=5A	50mA@1mohm,	CF		0.15	0.3	%
Phase error when PF=0.8 Capacitive	PF08err	Current lead 37°（PF=0.8）				0.5	%
Phase error when PF=0.5Inductive	PF05err	Current lags 60°（PF=0.5）				0.5	%
AC PSRR	ACPSRR	IP/N=100mV				0.1	%
DC PSRR	DCPSRR	VP/N=100mV				0.1	%
Vrms measurement Error	VRMSerr		CF1		0.3		%
Irms measurement Error	IRMSer		CF1		0.3		%
Analog Input Voltage(current)		Current differential input (peak)				50	mV
Analog Input Voltage(voltage)		voltage differential input (peak)				200	mV
Analog Input Impedance			VP/IP/IN		370		kΩ
SEL pull-down resistance		SEL(pull-down)			80		kΩ
Input Signal Bandwidth		(-3dB)			3.5		kHz
On-chip voltage reference	Vref		VREF		1.218		V
Input High Voltage		VDD=3.3V± 5%		2.6			V
Input Low Voltage		VDD=3.3V± 5%				0.8	V
Output High Voltage		VDD=3.3V± 5% IOH=5mA		VDD-0.5			V
Output Low Voltage		VDD=3.3V± 5% IOH=5mA				0.5	V
Over-current threshold		Current sampling			36		A

	resistor 1mΩ						
the frequency of over-current					6.7		kHz
response time of Over-current						200	ms

◆ THEORY OF OPERATION

◆ Principle of Energy Measure

In energy measure, the power information varying with time is calculated by a direct multiplication of the voltage signal and the current signal. Assume that the current signal and the voltage signal are cosine functions; V and I are the peak values of the voltage signal and the current signal; ω is the angle frequency of the input signals; the phase difference between the current signal and the voltage signal is expressed as Φ. Then the power is given as follows:

$$p(t) = V\cos(wt) \times I\cos(wt + \Phi)$$

Φ =0:

$$p(t) = \frac{VI}{2}(1 + \cos 2wt)$$

Φ ≠ 0:

$$p(t) = V\cos(wt) \times I\cos(wt + \Phi)$$
$$= V\cos(wt) \times \left[I\cos(wt)\cos(\Phi) + \sin(wt)\sin(\Phi)\right]$$
$$= \frac{VI}{2}(1 + \cos(2wt))\cos(\Phi) + VI\cos(wt)\sin(wt)\sin(\Phi)$$
$$= \frac{VI}{2}(1 + \cos(2wt))\cos(\Phi) + \frac{VI}{2}\sin(2wt)\sin(\Phi)$$

p(t) is called as the instantaneous power signal. The ideal p(t) consists of the dc component and ac component whose frequency is 2ω .

The current and voltage signals are sampled with high precision and converted from analog signals to digital signals. The instantaneous power signal p(t) is obtained after the digital signals are calculated by digital multiplier. In order to extract the real power component (i.e., the dc component), the instantaneous power signal is low-pass filtered, and then accumulated (e.g., by a counter) to generate real energy information. These information is sent to the digital-to-frequency converter module, and converted to a pulse signal of the certain frequency which is proportional to the real power.

Similarly, the voltage and current RMS are calculated and then sent to the digital-to-frequency converter module, converted to a pulse signal of the certain frequency which is proportional to the voltage and current RMS.

◆ Power Supply Monitor

BL0937 contains an on-chip power supply monitor. The power supply (VDD) is continuously monitored by BL0937. if the supply is less than 2.7V±5%, BL0937 will be reset. This is useful to ensure correct device startup at power-up and power-down. The power supply monitor has built in

hysteresis and filtering. This gives a high degree of immunity to false triggering due to noisy supplies. Under normal circumstances, the decoupling part of power supply should ensure that the ripple on VDD does not exceed 3.3V ±5%.

Internal Power Supply Monitor
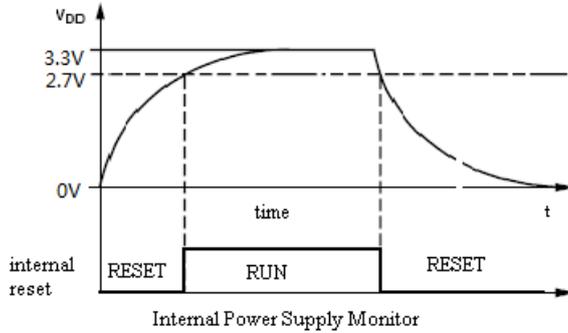
◆ **CHIP APPLICATIONS**

◆ **Typical Application**

BL0937 Typical application block diagram is shown below. Single 3.3V supply. The current signal is sampled through the alloy resistor connected to the IP and IN pins of BL0937. The voltage signal is sampled through the resistor divider network connected to the VP pin of BL0937. CF, CF1, SEL directly access to the MCU pin. CF/CF1 pulse cycle is measured to calculate the power, current RMS and voltage RMS.
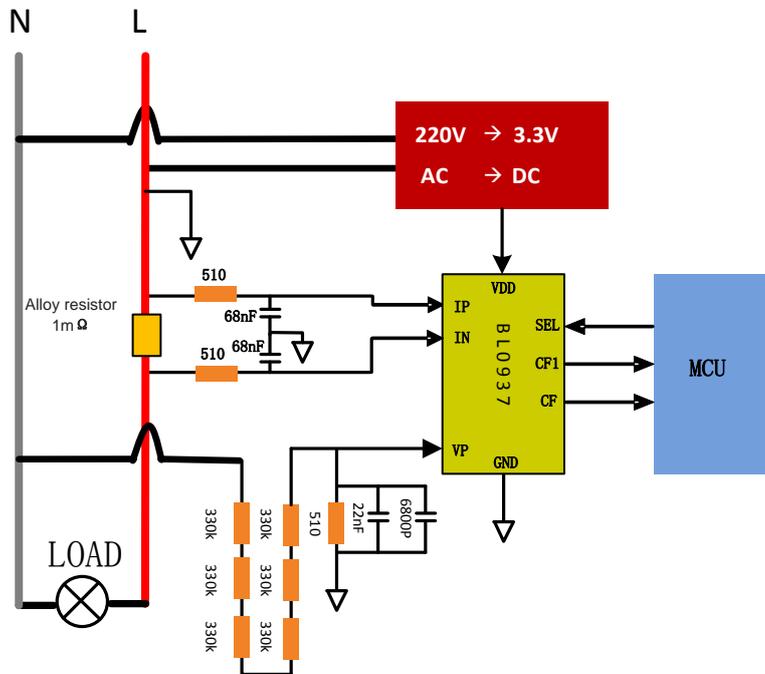
◆ **CF、CF1 frequency**

BL0937 calculates the product of two voltage signals (on current channel and voltage channel) and then low-pass filters this product to extract real power information. This real power information is then converted to a frequency. The frequency information is output on CF in the form of active high pulses. At the same time current RMS and voltage RMS are calculated and converted to a frequency, and output on CF1 in the form of active high pulses.

（1）active power pulse frequency formula：

$$F_{CF} = 1721506 * \frac{V(V) * V(I)}{V_{ref}^2}$$

（2）voltage RMS pulse frequency formula：

$$F_{CFU} = 15397 * \frac{V(V)}{V_{ref}}$$

（3）current RMS pulse frequency formula：

$$F_{CFI} = 94638 * \frac{V(I)}{V_{ref}}$$

V(V)——the voltage RMS of the voltage channel
V(I)——the voltage RMS of the current channel
Vref——reference voltage（1.218V）

◆ **Anti-creep**

BL0937 has a patented anti-creep design with a reasonable external hardware design to ensure that the noise power is not included in the energy pulse in the absence of current. The anti-creep threshold is 0.0035% of the active power corresponding to the full-scale input signal.

◆ **Over-current detection**

BL0937 has fast over-current detection function. BL0937 can detect the over-current within 200ms. The pin CF would output over-current signal when over-current occurs. This function helps to design the over-current protection circuit.

◆ **Current/voltage RMS output**

The current / voltage RMS is output from the pin CF1. When SEL = 0, the CF1 outputs a high frequency pulse of the current RMS; When SEL = 1, the CF1 outputs a high frequency pulse of the voltage RMS. The current and voltage RMS calculation modules of BL0937 are separate internally. SEL switching time < 10uS.