



Α	В	S
0	0	0
0	1	0
1	0	0
1	1	1

NAND



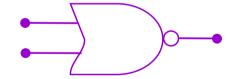
Α	В	S
0	0	1
0	1	1
1	0	1
1	1	0

OR



Α	В	S
0	0	0
0	1	1
1	0	1
1	1	1

NOR



Α	В	S
0	0	1
0	1	0
1	0	0
1	1	0