

```
set_property PACKAGE_PIN W5 [get_ports {Clock}]
set_property IOSTANDARD LVCMOS33 [get_ports {Clock}]
create_clock -add -name sys_clk_pin -period 10.00 -waveform {0 5} [get_ports {Clock}]
```

```
set_property PACKAGE_PIN L1 [get_ports {LED[15]}]
set_property IOSTANDARD LVCMOS33 [get_ports {LED[15]}]
```

```
set_property PACKAGE_PIN P1 [get_ports {LED[14]}]
set_property IOSTANDARD LVCMOS33 [get_ports {LED[14]}]
```

```
set_property PACKAGE_PIN N3 [get_ports {LED[13]}]
set_property IOSTANDARD LVCMOS33 [get_ports {LED[13]}]
```

```
set_property PACKAGE_PIN P3 [get_ports {LED[12]}]
set_property IOSTANDARD LVCMOS33 [get_ports {LED[12]}]
```

```
set_property PACKAGE_PIN U3 [get_ports {LED[11]}]
set_property IOSTANDARD LVCMOS33 [get_ports {LED[11]}]
```

```
set_property PACKAGE_PIN W3 [get_ports {LED[10]}]
set_property IOSTANDARD LVCMOS33 [get_ports {LED[10]}]
```

```
set_property PACKAGE_PIN V3 [get_ports {LED[9]}]
set_property IOSTANDARD LVCMOS33 [get_ports {LED[9]}]
```

```
set_property PACKAGE_PIN V13 [get_ports {LED[8]}]
set_property IOSTANDARD LVCMOS33 [get_ports {LED[8]}]
```

```
set_property PACKAGE_PIN V14 [get_ports {LED[7]}]
set_property IOSTANDARD LVCMOS33 [get_ports {LED[7]}]
```

```
set_property PACKAGE_PIN U14 [get_ports {LED[6]}]
set_property IOSTANDARD LVCMOS33 [get_ports {LED[6]}]
```

```
set_property PACKAGE_PIN U15 [get_ports {LED[5]}]
set_property IOSTANDARD LVCMOS33 [get_ports {LED[5]}]
```

```
set_property PACKAGE_PIN W18 [get_ports {LED[4]}]
set_property IOSTANDARD LVCMOS33 [get_ports {LED[4]}]
```

```
set_property PACKAGE_PIN V19 [get_ports {LED[3]}]
set_property IOSTANDARD LVCMOS33 [get_ports {LED[3]}]
```

set\_property PACKAGE\_PIN U19 [get\_ports {LED[2]]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {LED[2]]

set\_property PACKAGE\_PIN E19 [get\_ports {LED[1]]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {LED[1]]

set\_property PACKAGE\_PIN U16 [get\_ports {LED[0]]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {LED[0]]

set\_property PACKAGE\_PIN W15 [get\_ports {Switch[4]]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {Switch[4]]

set\_property PACKAGE\_PIN W17 [get\_ports {Switch[3]]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {Switch[3]]

set\_property PACKAGE\_PIN W16 [get\_ports {Switch[2]]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {Switch[2]]

set\_property PACKAGE\_PIN V16 [get\_ports {Switch[1]]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {Switch[1]]

set\_property PACKAGE\_PIN V17 [get\_ports {Switch[0]]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {Switch[0]]

set\_property PACKAGE\_PIN U18 [get\_ports {Rst}]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {Rst}]

set\_property PACKAGE\_PIN W19 [get\_ports {Ein}]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {Ein}]

set\_property PACKAGE\_PIN W4 [get\_ports {Anode[3]]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {Anode[3]]

set\_property PACKAGE\_PIN V4 [get\_ports {Anode[2]]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {Anode[2]]

set\_property PACKAGE\_PIN U4 [get\_ports {Anode[1]]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {Anode[1]]

set\_property PACKAGE\_PIN U2 [get\_ports {Anode[0]]  
set\_property IOSTANDARD LVCMOS33 [get\_ports {Anode[0]]

set\_property PACKAGE\_PIN W7 [get\_ports {Cathode[7]]

set\_property IOSTANDARD LVCMOS33 [get\_ports {Cathode[7]}]

set\_property PACKAGE\_PIN W6 [get\_ports {Cathode[6]}]

set\_property IOSTANDARD LVCMOS33 [get\_ports {Cathode[6]}]

set\_property PACKAGE\_PIN U8 [get\_ports {Cathode[5]}]

set\_property IOSTANDARD LVCMOS33 [get\_ports {Cathode[5]}]

set\_property PACKAGE\_PIN V8 [get\_ports {Cathode[4]}]

set\_property IOSTANDARD LVCMOS33 [get\_ports {Cathode[4]}]

set\_property PACKAGE\_PIN U5 [get\_ports {Cathode[3]}]

set\_property IOSTANDARD LVCMOS33 [get\_ports {Cathode[3]}]

set\_property PACKAGE\_PIN V5 [get\_ports {Cathode[2]}]

set\_property IOSTANDARD LVCMOS33 [get\_ports {Cathode[2]}]

set\_property PACKAGE\_PIN U7 [get\_ports {Cathode[1]}]

set\_property IOSTANDARD LVCMOS33 [get\_ports {Cathode[1]}]

set\_property PACKAGE\_PIN V7 [get\_ports {Cathode[0]}]

set\_property IOSTANDARD LVCMOS33 [get\_ports {Cathode[0]}]