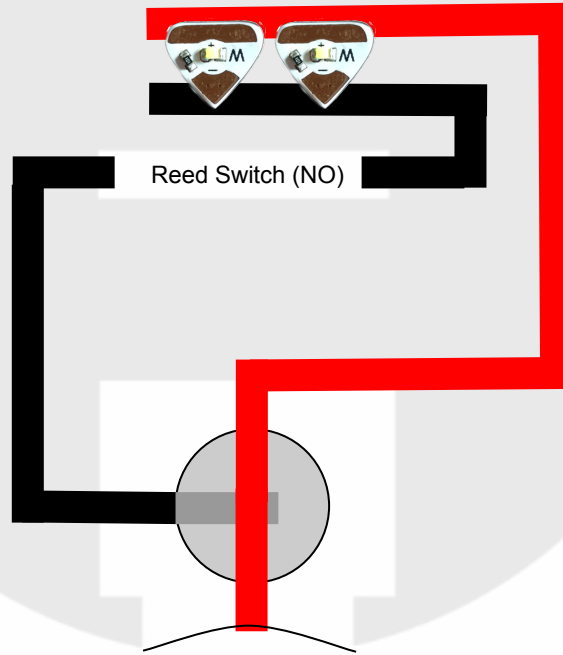


Overview of Circuit



Base Layer

